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## NOTES/COMMENTS:

Docket #	P027.03.CIP2+	By:	MEM	Faxing Date	September 9, 2005
Ser. No:	10/665,882	Filing Date:	09/19/2003	Inv(s)	Rinerson et al..
Title:	Resistive Memory Device with a Treated Interface				

The following is being transmitted to the U.S. Patent Office:

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1.	Supplemental Application Data Sheet	5
2.	Request for Continued Examination (RCE) Transmittal	1
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